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## APPLICANTS

Koji Kondo, Toyohashi-city, JAPAN;  
 Tomohiro Yokochi, Kariya-city, JAPAN;  
 Toshihiro Miyake, Inuyama-city, JAPAN;  
 Satoshi Takeuchi, Nagoya-city, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

Yes *[initials]*

This application is a DIV of 10/166,731 06/12/2002 PAT 6,680,441

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

Yes *[initials]*

JAPAN 2001-179118 06/13/2001  
 JAPAN 2001-199392 06/29/2001  
 JAPAN 2001-204023 07/04/2001  
 JAPAN 2002-62394 03/07/2002

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 02/04/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 22	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance				
Verified and Acknowledged <i>[Signature]</i> Examiner's Signature <i>[Initials]</i>				

## ADDRESS

23400

## TITLE

Method for manufacturing printed wiring board with embedded electric device

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